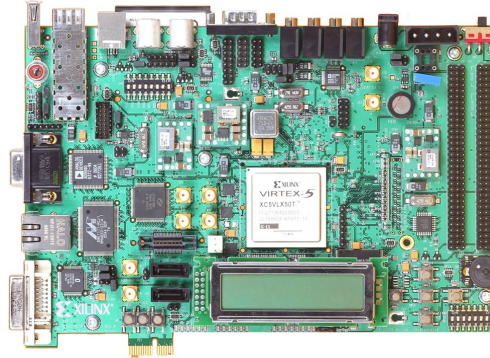


# Vantage Development Platform

## Evaluation and Software Development

**TRILINEAR**  
TECHNOLOGIES



The Trilinear Vantage Development Platform offers customers a versatile and cost-effective method for evaluating and testing the full suite Trilinear Intellectual Property cores. Using the integrated Compact Flash system, multiple cores may be evaluated using a single system. Image processing and video compression cores make use of the on-board ADC and DVI interfaces requiring no additional daughter cards.

Each Vantage system includes a host processor and peripheral suite running a flash-based ROM monitor that loads at power-up. The ROM monitor allows for the download of application code developed using GCC. This allows for simultaneous hardware and software evaluation efforts.

### Integrated Host Processor

- 32-bit RISC processor
- Embedded ROM monitor
- Up to 150MHz operation
- Internal AMBA 2.0 bus system

### Built-In ROM Monitor

- Detects integrated IP at run time
- Applications built using GCC may be downloaded using the serial port or Ethernet
- Includes traditional shell commands

### DDR-2 Memory System

- 256 MB PC4200
- 533 MHz Data Rate
- Peak bandwidth of 4.3 GB / sec

### Integrated 2D Accelerator

- Trilinear T211A Accelerator core
- 133MHz operation
- Includes polygon fill, BitBLT, rotate BLT, scaled BLT, block fill and vector drawing

### Onboard DVI Interface and LCD Controller

- Programmable display timing controller
- Display path supports multiple color depth

### Support Functions

- UART, Timers, GPIO
- PS/2 Controller
- Strata Flash Controller, 32 MB
- IIC Master
- Character LCD Interface

### Xilinx ml507 Based System

- Xilinx Virtex-5 LX70T
- Compact Flash card programming interface for rapid field upgrades

### Built in Networking

- Integrated Trilinear Ethernet MAC core optimized for video streaming applications
- 10 / 100 / 1G operation
- Marvell Ethernet PHY

